VxWorks System Boot

CPU: Motorola MVME3100-1152 - MPC8540

Version: VxWorks 6.2

BSP version: 2.0/1

Boot Loader version: 1.1.05

Creation date: Nov 20 2009, 09:06:31

Physical Memory: 0x10000000

Press any key to stop auto-boot...

0

auto-booting...

boot device : dc

unit number : 0

processor number : 0

host name : host

file name : vxWorks

inet on ethernet (e) : 172.20.20.65:ffffff00

host inet (h) : 172.20.20.64

user (u) : obi_supervisor

ftp password (pw) : obi_supervisor

flags (f) : 0x0

Attaching interface lo0... done

Attached IPv4 interface to mottsec unit 0

Loading... 6592528

Starting at 0x100000...

Attaching interface lo0... done

Attached IPv4 interface to mottsec unit 0

Adding 22486 symbols for standalone.

VxWorks

CPU: Motorola MVME3100-1152 - MPC8540

Runtime Name: VxWorks

Runtime Version: 6.2

BSP version: 2.0/1

Created: Jun 1 2011, 14:26:29

ED&R Policy Mode: Deployed

WDB Comm Type: WDB_COMM_END

WDB: Ready.

Starting OBI Supervisor application ...

****

* Varian Medical Systems Imaging Laboratory GmbH

* 5400 Baden, Switzerland

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* OBI Supervisor Release 1.5.19

* - Fault line tests on

* - ARCNET MCN System

* - Heartbeat sent via hardware

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UserInterface::StartupController::run

Domain::StartupController::run

Infrastructure::StartupController::run

System::StartupController::run

TPMC920::StartupController::run

tpmc920Drv() called:

Searching for 1498h/0398h/1498h/000Ah

Found at 1/0/0

Address: A0100080h --- Level: 4 --- Vector: 4

Devices found:

```
    #(0/0) DCB: 0FF01A90h --- A:A0100080h --- L:4 --- V:4
    #(1/1) DCB: 0FF01DA0h --- A:A0100088h --- L:4 --- V:4
    #(2/2) DCB: 0FF020B0h --- A:A0100090h --- L:4 --- V:4
    #(3/3) DCB: 0FF023C0h --- A:A0100098h --- L:4 --- V:4
    #(4/4) DCB: 0FF026D0h --- A:A01000A0h --- L:4 --- V:4
    #(5/5) DCB: 0FF029E0h --- A:A01000A8h --- L:4 --- V:4
    #(6/6) DCB: 0FF02CF0h --- A:A01000B0h --- L:4 --- V:4
    #(7/7) DCB: 0FF03000h --- A:A01000B8h --- L:4 --- V:4
TPMC920 serial driver successfully installed
vbgaDrv() called:
Found TPMC920 at 1/0/0
FPGA Base Addr.: 0xa0100200, Level: 4, Vector: 4, Int. Status Reg.: 0xa0100400
TPMC920 FPGA driver successfully installed
TPMC815::StartupController::run
TPMC815 Arcnet driver successfully installed
Creating Serial Devices

tpmc920Drv() called:
TPMC920 driver successfully installed
Creating device /serDev/0

tpmc920DevCreate() called:
  Device found (DCB:0FF01A90h)
Opening device /serDev/0
Adding device /serDev/0 with key /serDev/0
Creating device /serDev/1

tpmc920DevCreate() called:
  Device found (DCB:0FF01DA0h)
```

```
Opening device /serDev/1
Adding device /serDev/1 with key /serDev/1
Creating device /serDev/2

tpmc920DevCreate() called:
   Device found (DCB:0FF020B0h)
Opening device /serDev/2
Adding device /serDev/2 with key /serDev/2
Creating device /serDev/3

tpmc920DevCreate() called:
   Device found (DCB:0FF023C0h)
Opening device /serDev/3
Adding device /serDev/3 with key /serDev/3
Creating device /serDev/4

tpmc920DevCreate() called:
   Device found (DCB:0FF026D0h)
Opening device /serDev/4
Adding device /serDev/4 with key /serDev/4
Creating device /serDev/5

tpmc920DevCreate() called:
   Device found (DCB:0FF029E0h)
Opening device /serDev/5
Adding device /serDev/5 with key /serDev/5
Creating device /serDev/6

tpmc920DevCreate() called:
```

```
   Device found (DCB:0FF02CF0h)
Opening device /serDev/6
Adding device /serDev/6 with key /serDev/6
Creating device /serDev/7

tpmc920DevCreate() called:
   Device found (DCB:0FF03000h)
Opening device /serDev/7
Adding device /serDev/7 with key /serDev/7
Creating Arcnet Devices
TP815 Address: A1004100h --- Level: 6 --- Vector: 6
TPMC815 driver successfully installed
Creating device /arcDev/0

VxWorks login: Opening device /arcDev/0
Setting device /arcDev/0 online
Adding device /arcDev/0 with key /arcDev/0
Creating Digital I/O Devices
vbgaDrv() called:
TPMC920 FPGA driver successfully installed
Creating device /wd
vbgaDevCreate() called:
Devicename: /wd Base Address: 0xa0100200       Vector: 4      Level: 4
Port Id: 0  Port Type: Port Out
Signal:  Name:              Status:     Bit Mask:  Int Enbl:   Int Stat:
00      FPGA_VERSION        0xa01003fe  0x0000     0x00000000  0x00000000
Port Id: 1  Port Type: Line Out
Signal:  Name:              Status:     Bit Mask:  Int Enbl:   Int Stat:
00      WD_RESTART          0xa0100210  0x0040     0x00000000  0x00000000
```

Opening device /wd

Adding device /wd with key /wd

Creating device /hbgen

vbgaDevCreate() called:

Devicename: /hbgen Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Line In

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 HEART_B 0xa0100202 0x8000 0xa0100222 0xa0100232

Port Id: 1 Port Type: Line Out

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 HEARTBEAT_EN 0xa0100210 0x0020 0x00000000 0x00000000

Opening device /hbgen

Adding device /hbgen with key /hbgen

Creating device /isr0

vbgaDevCreate() called:

Devicename: /isr0 Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Line In

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 DOOR_ITLK 0xa0100200 0x0001 0xa0100220 0xa0100230

01 CTRL_AREA 0xa0100200 0x0002 0xa0100220 0xa0100230

02 XRAY_ON_2 0xa0100200 0x0004 0xa0100220 0xa0100230

03 XRAY_PWR_ON 0xa0100200 0x0008 0xa0100220 0xa0100230

04 FLUORO 0xa0100200 0x0010 0xa0100220 0xa0100230

05 XRAY 0xa0100200 0x0020 0xa0100220 0xa0100230

06 PREP 0xa0100200 0x0040 0xa0100220 0xa0100230

07 TOUCH_G 0xa0100200 0x0080 0xa0100220 0xa0100230

08 GTY_ENAB 0xa0100200 0x0100 0xa0100220 0xa0100230

09 CT_ENAB_IN 0xa0100200 0x0200 0xa0100220 0xa0100230

10 COLL_OVRD 0xa0100200 0x0400 0xa0100220 0xa0100230

11 P_5V_PWR_GOOD 0xa0100200 0x0800 0xa0100220 0xa0100230

12 P_12V_PWR_GOOD 0xa0100200 0x1000 0xa0100220 0xa0100230

13 M_12V_PWR_GOOD 0xa0100200 0x2000 0xa0100220 0xa0100230

14 SPARE_I1 0xa0100200 0x4000 0xa0100220 0xa0100230

15 OBI_OUT 0xa0100200 0x8000 0xa0100220 0xa0100230

Opening device /isr0

Adding device /isr0 with key /isr0

Creating device /isr1

vbgaDevCreate() called:

Devicename: /isr1 Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Line In

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 COL_L1 0xa0100202 0x0001 0xa0100222 0xa0100232

01 COL_L2 0xa0100202 0x0002 0xa0100222 0xa0100232

02 COL_L3 0xa0100202 0x0004 0xa0100222 0xa0100232

03 FAULT_L 0xa0100202 0x0008 0xa0100222 0xa0100232

04 RETR_OBI 0xa0100202 0x0010 0xa0100222 0xa0100232

05 RETR_PV 0xa0100202 0x0020 0xa0100222 0xa0100232

06 AUTOGO_OBI 0xa0100202 0x0040 0xa0100222 0xa0100232

07 AUTOGO_PV 0xa0100202 0x0080 0xa0100222 0xa0100232

08 MEB_CONS 0xa0100202 0x0100 0xa0100222 0xa0100232

09 MEB_PENDANT 0xa0100202 0x0200 0xa0100222 0xa0100232

10 MEB_CLINAC 0xa0100202 0x0400 0xa0100222 0xa0100232

11 GTY_ENC_I 0xa0100202 0x0800 0xa0100222 0xa0100232

12 TDC_GATE 0xa0100202 0x1000 0xa0100222 0xa0100232

13 GTY_ENC_A 0xa0100202 0x2000 0xa0100222 0xa0100232

14 GTY_ENC_B 0xa0100202 0x4000 0xa0100222 0xa0100232

Opening device /isr1

Adding device /isr1 with key /isr1

Creating device /isr2

vbgaDevCreate() called:

Devicename: /isr2 Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Line In

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 CLINAC_COLL_OVRD 0xa0100204 0x0001 0xa0100224 0xa0100234

01 OBI_COL1 0xa0100204 0x0002 0xa0100224 0xa0100234

02 CLINAC_CT_STATE 0xa0100204 0x0004 0xa0100224 0xa0100234

03 GTY_ROT_FOR_CT 0xa0100204 0x0008 0xa0100224 0xa0100234

04 COL_L4 0xa0100204 0x0010 0xa0100224 0xa0100234

Opening device /isr2

Adding device /isr2 with key /isr2

Creating device /osr

vbgaDevCreate() called:

Devicename: /osr Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Line Out

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 XRAY_EN_NOT 0xa0100210 0x0001 0x00000000 0x00000000

01 BUZZER 0xa0100210 0x0002 0x00000000 0x00000000

02 READY_FOR_CT 0xa0100210 0x0004 0x00000000 0x00000000

03 OBI_STATUS0 0xa0100210 0x0008 0x00000000 0x00000000

04 SPARE_O2 0xa0100210 0x0010 0x00000000 0x00000000

05 SPARE_X1 0xa0100210 0x0020 0x00000000 0x00000000

06 SPARE_X2 0xa0100210 0x0040 0x00000000 0x00000000

07 IL_ARMS 0xa0100210 0x0080 0x00000000 0x00000000

08 CT_ENAB_OUT 0xa0100210 0x0100 0x00000000 0x00000000

09 SPARE_O4 0xa0100210 0x0200 0x00000000 0x00000000

10 OBI_GANTEN 0xa0100210 0x0400 0x00000000 0x00000000

Opening device /osr

Adding device /osr with key /osr

Creating device /tfr

vbgaDevCreate() called:

Devicename: /tfr Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Port Out

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 TFR 0xa0100240 0x0000 0x00000000 0x00000000

Opening device /tfr

Adding device /tfr with key /tfr

Creating device /tirr

vbgaDevCreate() called:

Devicename: /tirr Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Port Out

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 TIRR 0xa0100242 0x0000 0x00000000 0x00000000

Opening device /tirr

Adding device /tirr with key /tirr

Creating device /mgmr

vbgaDevCreate() called:

Devicename: /mgmr Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Line Out

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 MG1_KVD 0xa0100250 0x0001 0x00000000 0x00000000

01 MG2_KVD 0xa0100250 0x0002 0x00000000 0x00000000

02 MG3_KVD 0xa0100250 0x0004 0x00000000 0x00000000

03 MG4_KVD 0xa0100250 0x0008 0x00000000 0x00000000

04 MG1_KVS 0xa0100250 0x0010 0x00000000 0x00000000

05 MG2_KVS 0xa0100250 0x0020 0x00000000 0x00000000

06 MG3_KVS 0xa0100250 0x0040 0x00000000 0x00000000

07 MG4_KVS 0xa0100250 0x0080 0x00000000 0x00000000

08 MG1_MVD 0xa0100250 0x0100 0x00000000 0x00000000

09 MG2_MVD 0xa0100250 0x0200 0x00000000 0x00000000

10 MG3_MVD 0xa0100250 0x0400 0x00000000 0x00000000

11 MG4_MVD 0xa0100250 0x0800 0x00000000 0x00000000

Opening device /mgmr

Adding device /mgmr with key /mgmr

Creating device /lsr

vbgaDevCreate() called:

Devicename: /lsr Base Address: 0xa0100200 Vector: 4 Level: 4

Port Id: 0 Port Type: Port Out

Signal: Name: Status: Bit Mask: Int Enbl: Int Stat:

00 LED 0xa0100260 0x0000 0x00000000 0x00000000

Opening device /lsr

Adding device /lsr with key /lsr

Creating Memory Devices

Creating NvRam

Creating Persistency

Activating tPersistency

Creating System

Creating HTTP Server

Creating Arcnet Transmitter

Activating tArcnetTx

Creating Arcnet Receiver

Activating tArcnetRx

Creating Common Pages

Creating Main Pages

SystemBuilder::initObjects - Initializing Blades Proxy

Activating tBlades

SystemBuilder::initObjects - Initializing MVD Proxy

Activating tMegaVoltDetector

SystemBuilder::initObjects - Initializing KVD Proxy

Activating tKiloVoltDetector

SystemBuilder::initObjects - Initializing KVS Proxy

Activating tKiloVoltSource

SystemBuilder::createBroadcaster - Creating Broadcaster

Activating tBroadcaster

SvStStartup::Init - Top level objects created

SvStStartup::Init - Initializing top level objects

SystemBuilder::initObjects - Initializing User LED Control

*** starting tLEDS

SystemBuilder::initObjects - Initializing Safety Box Driver

SystemBuilder::initObjects - Initializing Safety Box Service

SystemBuilder::initObjects - Initializing Watchdog

SystemBuilder::initObjects - Initializing Fault Management

SystemBuilder::initObjects - Initializing Interlock Management

Activating tIAS3

SystemBuilder::initObjects - Initializing Subsystem Registry

HtSystemBuilder::initObjects - Initializing Query Node Info

tSystemBuilder::initObjects - Initializing Comms Service

pSeSystemBuilder::initObjects - Initializing Ethernet Service WS

rver started

SystemBuilder::initObjects - Initializing Ethernet Service ST

SystemBuilder::initObjects - Initializing Ethernet WS

SystemBuilder::initObjects - Initializing Ethernet ST

SystemBuilder::initObjects - Initializing Configuration Management

SystemBuilder::initObjects - Initializing Supervisory

SystemBuilder::initObjects - Initializing Axis Arbitration

SystemBuilder::initObjects - Initializing Motion Sequencing

SystemBuilder::initObjects - Initializing Collision Management

SystemBuilder::initObjects - Initializing Node Manager

SystemBuilder::initObjects - Initializing Image Acquisition System Proxy

SystemBuilder::initObjects - Initializing Blade Tracking

SystemBuilder::initObjects - Initializing X Ray Buzzer

SystemBuilder::initObjects - Initializing Check Request

SystemBuilder::initObjects - Initializing Requester

SystemBuilder::initObjects - Initializing Position Order Handler

SystemBuilder::initObjects - Initializing CBCT

SystemBuilder::initObjects - Initializing Serial Com Manager

SystemBuilder::initObjects - Initializing Pendant

SystemBuilder::initObjects - Initializing Console

SystemBuilder::initObjects - Initializing Clinac

SystemBuilder::initObjects - Initializing Position Store

SystemBuilder::initObjects - Initializing Monolith Web Server

SvStStartup::Init - Starting initial tasks

*** starting tSysLogWrite - initial state is '1'

*** starting tEtReadWS

*** starting tEtWriteWS

*** starting tEtReadST

*** starting tEtWriteST

*** starting tSafetyService

*** starting tStartup

*** starting tInterlockManagement

*** starting tFaultManagement

*** starting tRequester

*** starting tPendant

*** starting tClinac

*** starting tConsole

SvStStartup::buildNetworkMap() - IO Map / found subSys; eBladeMCN

SvStStartup::buildNetworkMap() - IO Map / found subSys; eKVSourceArmMCN

SvStStartup::buildNetworkMap() - IO Map / found subSys; eKVDetectorArmMCN

SvStStartup::buildNetworkMap() - IO Map / found subSys; eMVDetectorArmMCN

SvStStartup::buildNetworkMap() - IO Map / found subSys; eSupervisor

SvStStartup::InitiateTestSequence - initial state of faultline = 0

SvStStartup::StartFaultLineTests start test on first node

SvStStartup::SetState transit from eWaitInitObjects To eWaitFaultLineAsserted

SvSSvStStartup::InitiateFaultLineAssert - requesting fault line pull from node (tSt88artup::NotifyColli)

sionLineState - c Faultline Assert test - ollision line 88200

has changed state to 0

SvStStartup::NotifyCollisionLineState - called in wrong state 4 (MCN=88)

SvStStartup::NotifyCollisionLineState - collision line 196 has changed state to 0

SvStStartup::NotifyCollisionLineState - called in wrong state 4 (MCN=88)

SvStStartup::NotifyCollisionLineState - collision line 198 has changed state to 0

SvStStartup::NotifyCollisionLineState - called in wrong state 4 (MCN=88)

SvStStartup::NotifyFaultLineState 0

SvStStartup::NotifyFaultLineState Fault line deasserted unexpectedly in state eWaitFaultLineAsserted

SvStStartup::NotifyFaultLineState 1

Faultline Assert OK - 88

SvStStartup::SetState transit from eWaitFaultLineAsserted To eWaitFaultLineDeasserted

Faultline DeAssert test - 88

SvStStartup::NotifyFaultLineState 0

Faultline DeAssert OK - 88

SvStStartup::SetState transit from eWaitFaultLineDeasserted To eWaitFaultLineAsserted

SvStStartup::InitiateFaultLineAssert - requesting fault line pull from node (c4)

Faultline Assert test - c4

SvStStartup::NotifyFaultLineState 1

Faultline Assert OK - c4

SvStStartup::SetState transit from eWaitFaultLineAsserted To eWaitFaultLineDeasserted

Faultline DeAssert test - c4

SvStStartup::NotifyFaultLineState 0

Faultline DeAssert OK - c4

SvStStartup::SetState transit from eWaitFaultLineDeasserted To eWaitFaultLineAsserted

SvStStartup::InitiateFaultLineAssert - requesting fault line pull from node (c6)

Faultline Assert test - c6

SvStStartup::NotifyFaultLineState 1

Faultline Assert OK - c6

SvStStartup::SetState transit from eWaitFaultLineAsserted To eWaitFaultLineDeasserted

Faultline DeAssert test - c6

SvStStartup::NotifyFaultLineState 0

Faultline DeAssert OK - c6

SvStStartup::SetState transit from eWaitFaultLineDeasserted To eWaitFaultLineAsserted

SvStStartup::InitiateFaultLineAssert - requesting fault line pull from node (c8)

Faultline Assert test - c8

SvStStartup::NotifyFaultLineState 1

Faultline Assert OK - c8

SvStStartup::SetState transit from eWaitFaultLineAsserted To eWaitFaultLineDeasserted

Faultline DeAssert test - c8

SvStStartup::NotifyFaultLineState 0

Faultline DeAssert OK - c8

SvStStartup::SetState transit from eWaitFaultLineDeasserted To eWaitCollisionTimeout

Collisionline Assert test - 88

Collision Line no response test OK - 88

Collisionline DeAssert test - 88

SvStStartup::OnTimeout - initiating collision line test on c4

SvStStartup::SetState transit from eWaitCollisionTimeout To eWaitAssertDSACollision

Collisionline Assert test - c4

SvStStartup::NotifyCollisionLineState - collision line 196 has changed state to 1

DS Arm c4 Collision Line Assert test OK

SvStStartup::SetState transit from eWaitAssertDSACollision To eWaitReleaseDSACollision

Collisionline DeAssert test - c4

SvStStartup::NotifyCollisionLineState - collision line 196 has changed state to 0

DS Arm c4 Collision Line DeAssert test OK

SvStStartup::SetState transit from eWaitReleaseDSACollision To eWaitAssertDSACollision

Collisionline Assert test - c6

SvStStartup::NotifyCollisionLineState - collision line 198 has changed state to 1

DS Arm c6 Collision Line Assert test OK

SvStStartup::SetState transit from eWaitAssertDSACollision To eWaitReleaseDSACollision

Collisionline DeAssert test - c6

SvStStartup::NotifyCollisionLineState - collision line 198 has changed state to 0

DS Arm c6 Collision Line DeAssert test OK

SvStStartup::SetState transit from eWaitReleaseDSACollision To eWaitAssertDSACollision

Collisionline Assert test - c8

SvStStartup::NotifyCollisionLineState - collision line 200 has changed state to 1

DS Arm c8 Collision Line Assert test OK

SvStStartup::SetState transit from eWaitAssertDSACollision To eWaitReleaseDSACollision

Collisionline DeAssert test - c8

SvStStartup::NotifyCollisionLineState - collision line 200 has changed state to 0

DS Arm c8 Collision Line DeAssert test OK

Fault/Collision Line startup tests complete

SvStStartup::Run

SvStStartup::SetState transit from eWaitReleaseDSACollision To eRun

SvStStartup::Run - Start Remaining Tasks

*** starting tReportPositions

*** starting tBladeTrackingControl

*** starting tExcBladeTracking

*** starting tSubsystemRegistry

*** starting tSupervisory

*** starting tAxisArbitration

*** starting tMotionSequencing

*** starting tXRayBuzzer

SvStStartup::Run - Safetybox

/hbgen: creating read semaphore

/hbgenSvStStartup::Ru: connecting interrupt

n - Comms Servic/hbgene

: enabling interrupt

/hbgen - HEART_B : enabling interrupt

+++ tStartup completed

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