

interrupt: Link Fÿ

PPCBoot 1.1.6 (Apr 7 2006 - 15:09:10)

CPU: IBM PowerPC 440 Rev. C
Board: Artesyn Technologies PM/PPC-440gp
Artesyn Monitor Version 1.4.0
VCO: 800 MHz
CPU: 400 MHz
PLB: 133 MHz
OPB: 66 MHz
EPB: 66 MHz

I2C: ready
DRAM: ECC InitDone 256 MB
FLASH: 64 MB

Initializing board as Monarch....

Scanning for PCI devices....

Scanning PCI devices on bus 0

| LOCALE | VEND | DEV | REV | BASE0 | BASE1 | BASE2 | BASE3 | BASE4 |
|--------|------|-----|-----|-------|-------|-------|-------|-------|
|--------|------|-----|-----|-------|-------|-------|-------|-------|

| | | | | | | | | |
|----------|-------|-------|-------|----------|----------|-------|-------|-------|
| ----- | ----- | ----- | ----- | ----- | ----- | ----- | ----- | ----- |
| 00,06,0 | 10b5 | 9030 | 0a | 80000000 | b0800000 | | | |
| 80100000 | | | | | | | | |

BusDevFunc: f800

```

In:    serial
Out:   serial
Err:   serial
Net:   EMAC0, EMAC1
BEDBUG:ready
Hit any key to stop autoboot:  0
## Starting application at 0xd3f40004 ...
pctest:40004, pctestMain:40094,startOffs: 90
-Loading Default PLX Local Configuration Registers ...
Done
Checking FPGA Status ... CP II FPGA Firmware Revision = 00001436
0 1 2 3
*** Press any key for
... 1          Starting CP2 Power On Self Test Version 4.62 ...

PLX9030 Tests ...          ..... PASSED
Test IPCU Debug Registers ...          PASSED
Mem Test ...                PASSED
IPU PASS_THRU TEST ...          NA
IPU Pipeline and Corrections Test ...  NA
Fast DDR_SDRAM Test ...          PASSED
Front Panel LED Display Test (Verify all on) ...
Loading Operating System (messages on CP2 Serial 2) ...
... Goodbye
Attached TCP/IP interface to emac unit 0
Attaching interface lo0...done
Backplane IP address required if no Proxy ARP

Adding 5473 symbols for standalone.
0x87ffe00 (tRootTask):

0x87ffe00 (tRootTask):
*****
0x87ffe00 (tRootTask):
0x87ffe00 (tRootTask):      Command Processor 2 System Software
0x87ffe00 (tRootTask):
0x87ffe00 (tRootTask):      Revision K.05, Build 61
0x87ffe00 (tRootTask):
0x87ffe00 (tRootTask):      Feb 25 2008  17:58:08
0x87ffe00 (tRootTask):
0x87ffe00 (tRootTask):
*****
0x87ffe00 (tRootTask):

0x87ffe00 (tRootTask): Initializing System Controller...
-> 0x87ffe00 (tRootTask): Loading Receptor firmware version :
4030A_CB Ver2 Bld
29
0x87ffe00 (tRootTask):

CheckReceptorHealth is Active.
0x87ffe00 (tRootTask): INFO: Testing Receptor Imager

```

```
0x87ffe00 (tRootTask): Error - Receptor Error
CheckReceptorHealth: STD = 2181, must be less than 1377
0x87ffe00 (tRootTask): info = File: SystemSoftwareFile of size 1895808
0x87ffe00 (tRootTask): info = File: ConfigDataFile of size 22800
0x87ffe00 (tRootTask): info = File: RcptFirmware of size 468934
0x87ffe00 (tRootTask): info = File: defectMapFile0 of size 196608
0x87ffe00 (tRootTask): info = File: defectMapFile1 of size 196608
0x87ffe00 (tRootTask): info = File: defectMapFile2 of size 196608
0x87ffe00 (tRootTask): info = File: defectMapFile3 of size 196608
0x87ffe00 (tRootTask): info = File: defectMapFile4 of size 786432
0x87ffe00 (tRootTask): info = File: defectMapFile5 of size 786432
0x87ffe00 (tRootTask): info = File: defectMapFile6 of size 393216
0x87ffe00 (tRootTask): info = File: defectMapFile7 of size 196608
0x87ffe00 (tRootTask): Command Processor IP = 172.20.20.50

0xff974c8 (tStateRcvTsk): SetPanelHealthy value in GcDirect is 1
0xff974c8 (tStateRcvTsk): SetReceptorReady value in GcDirect is 0
```